

REMARKS

This Amendment, filed in reply to the Office Action dated January 18, 2008, is believed to be fully responsive to each point of rejection raised therein. Accordingly, reconsideration and allowance are respectfully requested. In the present Amendment, Applicant amends claims 1 and 19. No new matter is added.

I. Preliminary Matters

The Examiner did not indicate acceptance of the drawings filed with the application on March 17, 2004. Therefore, Applicant respectfully requests the Examiner to indicate that the drawings are accepted in the next office communication from the USPTO.

II. Claim Rejections - 35 U.S.C. § 102

Claims 1, 2, and 7-12 have been rejected under 35 U.S.C. § 102(b) as allegedly being anticipated by U.S. Patent No. 5,796,767 to Aizawa. Claims 1 and 2 are the only independent claims in the application. In this Amendment, Applicant amends claim 1 to add features of claim 19, which the Examiner has indicated contains allowable subject matter. For the foregoing reasons, Applicant respectfully submits that claims 1, 2, and 7-12 are patentable over the cited art of record.

Aizawa discloses a driver circuit of a light-emitting device. The circuit includes

a reference current source for generating a reference current, a cascode current source circuit for generating a driving current for the light-emitting device with the use of the reference current, and an input circuit for switching the driving current according to a data signal. The cascode current source circuit has a first current mirror formed by first and second transistors and a second current mirror formed by third and fourth transistors (Aizawa at Abstract; *see also* FIG. 4 and associated disclosure).

With respect to amended **claim 1**, Applicant respectfully submits that the cited art of record does not disclose “A display, comprising: organic EL elements that are arranged in a matrix; current drive circuits and scan circuits for causing drive currents to flow to said organic EL elements,” wherein the current drive circuit comprises the elements recited. Even assuming, *arguendo*, that Aizawa discloses the current drive circuit recited in amended claim 1, Aizawa does not disclose the display recited therein. Applicant respectfully submits that, at least for this reason, amended claim 1 is patentable over the cited art of record.

With respect to **claims 7, 9 and 11**, Applicant respectfully submits that they are patentable, at least by virtue of their dependency on amended claim 1, but also for their additionally recited features.

With respect to **claim 2**, Applicant respectfully submits that Aizawa does not disclose, at least, “a current supply that causes a reference current to flow to said second transistor,” as recited therein. The Examiner alleges that Aizawa discloses these elements on FIG. 14, items 4 (current supply) and Q4 (second transistor) (*see* Office Action at page 3). However, in Aizawa, the current that flows through transistor Q4 is not a reference current caused by the current supply (*see* FIG. 4). Instead, the current that flows through transistor Q4 (current I1) is a mirror current (*see* FIG. 4). Applicant respectfully submits that Aizawa’s reference current (I_{ref0}) flows only though transistor Q1, and does not flow through transistor Q4 (*see* FIG. 4). At least for this reason, Applicant respectfully submits that Aizawa does not disclose claim 2 of the subject application.

Furthermore, Applicant respectfully submits that Aizawa also does not disclose “a switch means that is provided between a drain terminal of said third transistor and a source terminal of

said fourth transistor,” as recited in claim 2. The Examiner alleges that Aizawa discloses these elements on FIG. 14, item Q9 (*see* Office Action at page 3). This is simply incorrect.

First, in Aizawa, transistor Q9 is provided between the gate terminal of transistor Q6 and the gate terminal of transistor Q8 (*see* FIG. 4). Therefore, transistor Q9 does not disclose “a switch means that is provided between a drain terminal of said third transistor and a source terminal of said fourth transistor,” as recited in claim 2.

Second, the Examiner alleges that the “third transistor” of claim 2 is disclosed by Aizawa’s transistor Q7, and further alleges that the “fourth transistor” of claim 2 is disclosed by Aizaya’s transistor Q8 (*see* Office Action at page 3). Even assuming *arguendo* that Aizawa makes such disclosure, Applicant notes that transistor Q9 is provided between transistor Q6 and transistor Q8 (fourth transistor), and is not coupled to any terminal of transistor Q7 (third transistor) (*see* FIG. 4). Therefore, Aizawa’s transistor Q9 does not disclose “a switch means that is provided between a drain terminal of said third transistor and a source terminal of said fourth transistor.”

For these additional reasons, Applicant respectfully submits that Aizawa does not disclose claim 2 of the subject application.

With respect to **claims 8, 10 and 12**, Applicant respectfully submits that they are patentable, at least by virtue of their dependency on claim 2, but also for their additionally recited features.

III. Allowable Subject Matter

The Examiner indicated that claims 3-6 and 13-20 contain allowable subject matter and would be allowed if rewritten in independent form.

With respect to **claim 19**, in this Amendment, Applicant has amended the claim to incorporate all of the features of its original base claim (i.e., original claim 1). Accordingly, independent claim 19 should now be in condition for immediate allowance and such action is hereby solicited.

With respect to **claims 3-6, 13-18 and 20**, they refer to independent claim 2. Applicant respectfully requests the Examiner to hold in abeyance any rewriting of these claims until the Examiner has had a chance to reconsider and withdraw the rejection regarding claim 2.

IV. Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

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